

UM621024 Series 128K×8 CMOS SRAM

Features

- Single +5 volt power supply
- Access times: 70/85/100 ns (max.)
- Current:
 - Standard version: Operating: 70 mA (max.)
Standby: 2 mA (max.)
 - Low power version: Operating: 70 mA (max.)
Standby: 100 μ A (max.)
- Fully static operation, no clock or refreshing required
- Directly TTL compatible: All inputs and outputs
- Common I/O using three-state output
- Output enable and two chip select inputs for easy application
- Data retention voltage: 2V (min.) for low power version.
- Available in 32 pin DIP or SOP packages

Pin Configuration

