



DS-ICE27LC512

512K bit, (64KX8) OTP EPROM

Description

The ICE27LC512 is a low-power, high-performance 512k(524288) bit one-time programmable read only memory (OTP EPROM) organized as 64K by 8 bits. It is single 3.3V power supply in normal read mode operation. Any byte can be accessed in less than 100ns. The ICE27LC512 typically consumes 10mA , standby mode supply current typically 1μA. Two lines control (CE, OE) to give designers the flexibility to prevent bus contention. Programming time is typically only 100 μs/byte. The Integrated Product Identification Code electronically identifies the device and manufacturer. This feature is used by industry standard programming equipment to select the proper programming algorithms and voltages.

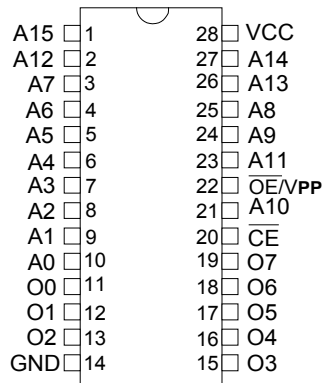
Features

- Low-Power consumption
 - 1 μ A Typ. Standby
 - 10 mA max. Active at 5MHz
- JEDEC Standard Packages
 - 28-Lead 600-mil PDIP
 - 32-Lead PLCC
 - 28-Lead TSOP
- Operating voltage : 3.3V ±10%
- High Reliability CMOS Technology
 - 2000V ESD Protection
 - 200 mA Latchup Immunity
- Programming time : 100 μ s/byte (typical)
- CMOS and TTL Compatible Inputs and Outputs
- Integrated Product Identification Code

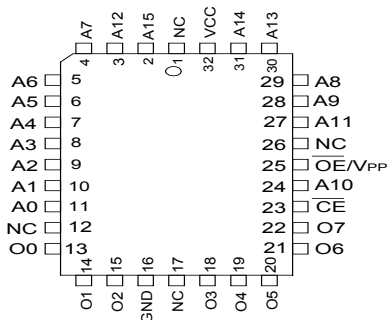
Pin Configurations

Pin Name	Function
A0 – A15	Addresses
O0 – O7	Outputs
/CE	Chip Enable
/OE/VPP	Output Enable
NC	No Connect

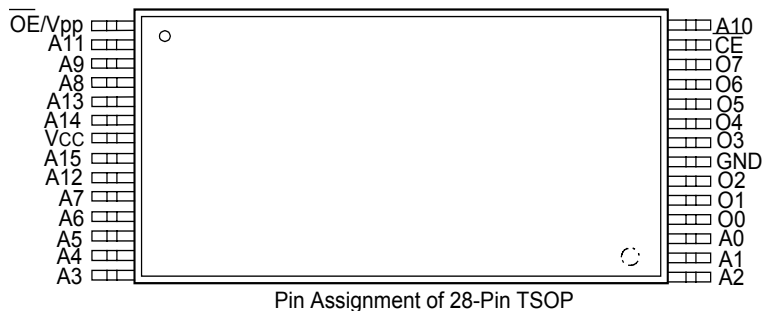
PDIP, SOLT Top View



PLCC Top View

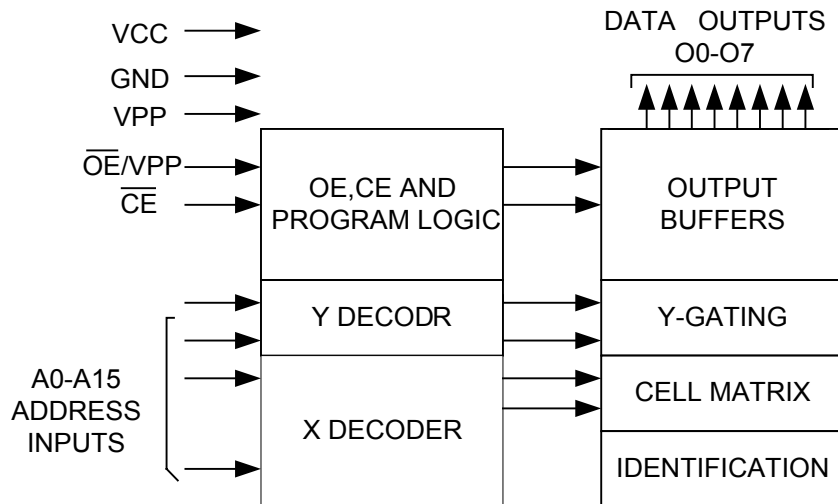


TSOP Top View



Pin Assignment of 28-Pin TSOP

Block Diagram



Absolute Maximum Rating

Operation Temperature Commercial	0 to +70
Storage Temperature	-65 to +125
Voltage on Any Pin with Respect to Ground	-0.6V to +7.0V ⁽¹⁾
Vpp Supply Voltage with Respect to Ground.....	-0.6V to +13.5V ⁽¹⁾

Operating Modes

Mode\Pin	$\overline{\text{CE}}$	$\overline{\text{OE/VPP}}$	Ai	Outputs
Read	V _{IL}	V _{IL}	Ai	D _{OUT}
Output Disable	X	V _{IH}	X	High Z
Standby	V _{IH}	X	X	High Z
Rapid Program ⁽²⁾	V _{IL}	V _{PP}	Ai	D _{IN}
PGM Inhibit	V _{IH}	X	X	High Z
Product Identification ⁽⁴⁾	V _{IL}	V _{IL}	A9 = V _H ⁽³⁾ A0,A1 = V _{IH} or V _{IL} A2 – A15 = V _{IL}	Identification Code

- Notes: 1. X can be V_{IL} or V_{IH}.
 2. Refer to Programming Characteristics.
 3. V_H = 12 ± 0.5V.
 4. See Product Identification Code item.

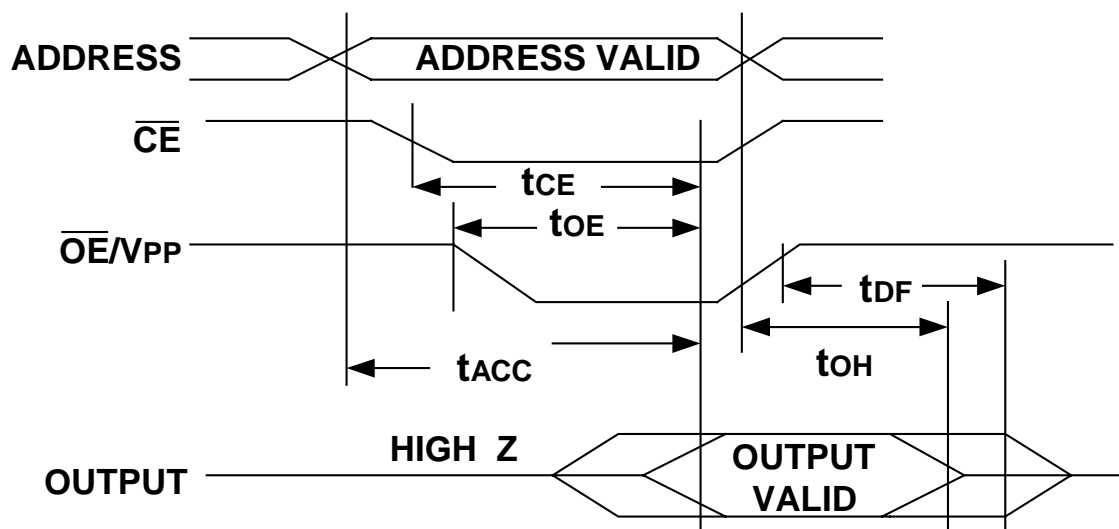
DC and Operating Characteristics for Read Operation

Symbol	Parameter	Condition		Min	Max	Units
I_{LI}	Input Load Current	$V_{IN} = 0V$ to V_{CC}	Com.		± 1	μA
I_{LO}	Output Leakage Current	$V_{OUT} = 0V$ to V_{CC}	Com.		± 5	μA
I_{SB}	$V_{CC}^{(1)}$ Standby Current	I_{SB1} (CMOS), /CE = $V_{CC} \pm 0.3V$			10	μA
		I_{SB2} (TTL), /CE = 2.0 to $V_{CC} + 0.3V$			500	μA
I_{CC}	V_{CC} Active Current	$f = 5MHz$, $I_{OUT} = 0mA$, /CE = V_{IL}			10	mA
V_{IL}	Input Low Voltage			-0.6	0.8	V
V_{IH}	Input High Voltage			2.0	$V_{CC} + 0.3$	V
V_{OL}	Output Low Voltage	$I_{OL} = 2.1 mA$			0.4	V
V_{OH}	Output High Voltage	$I_{OH} = -400 \mu A$		2.4		V

Notes:

1. V_{CC} must be applied simultaneously or before V_{pp} and removed simultaneously or after V_{pp} .
2. V_{pp} may be connected directly to V_{CC} except during programming. The supply current would then be the sum of I_{CC} and I_{PP} .

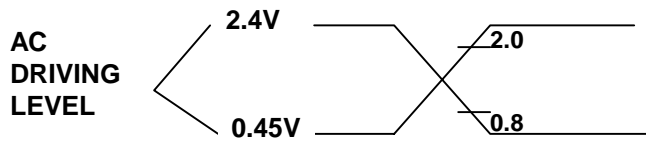
AC Waveforms for Read Operation ⁽¹⁾



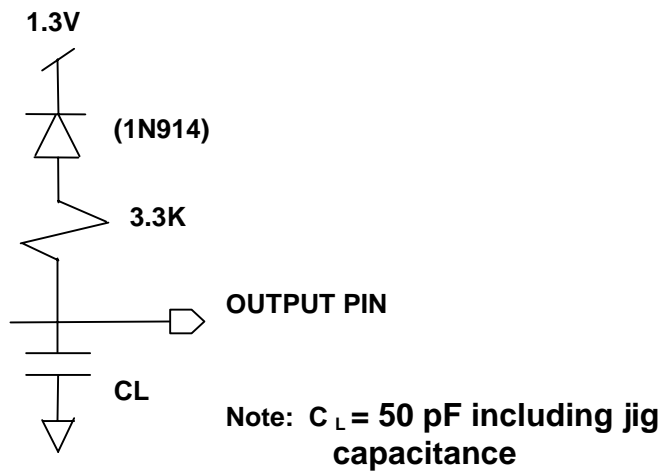
Notes:

1. $\overline{OE/VPP}$ may be delayed up to $t_{CE} - t_{OE}$ after the falling edge of \overline{CE} without impact on t_{CE} .
2. $\overline{OE/VPP}$ may be delayed up to $t_{ACC} - t_{OE}$ after the address is valid without impact on t_{ACC} .
3. This parameter is only sampled and is not 100% tested.
4. Output float is defined as the point when data is no longer driven.

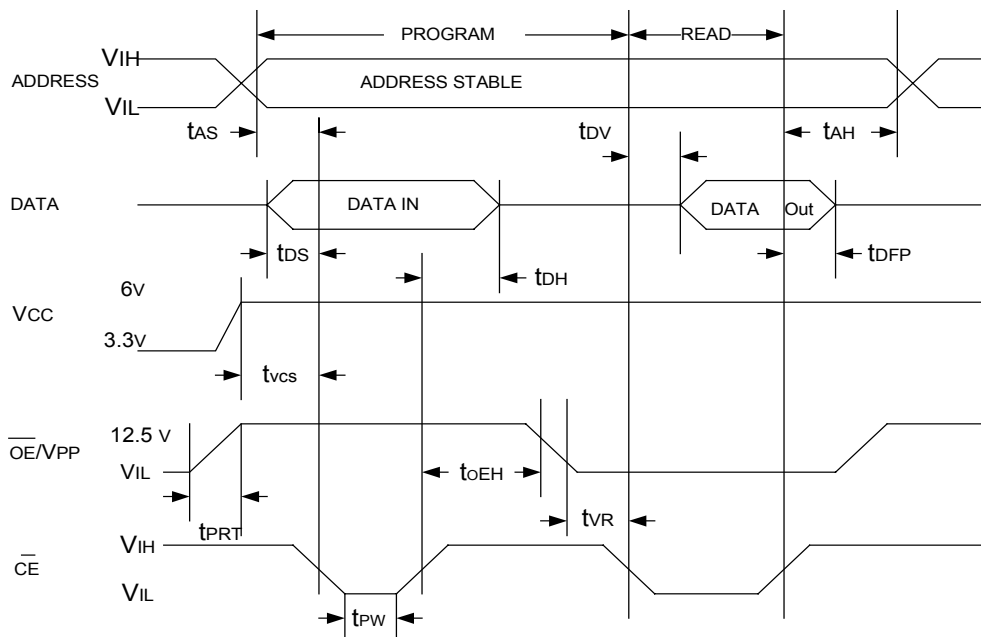
Input Test Waveforms and Measurement Levels



Output Test Load



Programming Waveforms



Notes:

1. The Input Timing Reference is 0.8V for V_{IL} and 2.0V V_{IH} .
2. t_{OE} and t_{DFP} are characteristics of the device but must be accommodated by the programmer.
3. When programming the ICE27LC512 at 0.1 μ F capacitor is required across V_{pp} and ground to suppress spurious voltage transients.

DC Programming Characteristics
 $T_A = 25 \pm 5$, $V_{CC} = 5.5 \pm 0.5V$, $V_{PP} = 12 \pm 0.5V$

Symbol	Parameter	Test Conditions	Limits		Units
			Min	Max	
I_{LI}	Input Load Current	$I_{IN} = V_{IL}, V_{IH}$		± 10	μA
V_{IL}	Input Low Level		-0.6	0.8	V
V_{IH}	Input High Level		2.0	$V_{CC} + 1$	V
V_{OL}	Output Low Voltage	$I_{OL} = 2.1 \text{ mA}$		0.4	V
V_{OH}	Output High Voltage	$I_{OH} = -400 \mu A$	2.4		V
I_{CC2}	Vcc Supply Current (Program and Verify)			20	mA
I_{PP2}	/OE/VPP Current	/CE = V_{IL}		10	mA
V_{ID}	A9 Product Identification Voltage		11.0	12.5	V

AC Characteristics for Read Operation

Symbol	Parameter	condition	-70		-90		-120		Units
			Min	Max	Min	Max	Min	Max	
$t_{ACC}^{(3)}$	Address to Output Delay	/CE=/OE/VPP= V_{IL}		40		50		50	ns
$t_{CE}^{(2)}$	/CE to Output Delay	/OE/VPP= V_{IL}		70		90		120	ns
$t_{OE}^{(2)(3)}$	/OE/Vpp to Output Delay	/CE= V_{IL}		70		90		120	ns
$t_{DF}^{(4)(5)}$	/OE/VPP or /CE Hight to Output Float,whichever occurred first			25		25		30	ns
t_{OH}	Output Hold from Address,/CE or /OE/VPP whichever occurred first		7		0		0		ns

Notes:2,3,4,5.-see AC Waveforms for Read Operation.

AC Programming Characteristics

$T_A = 25 \pm 5$, $V_{CC} = 5.5 \pm 0.5V$, $V_{PP} = 12.0 \pm 0.5V$

Symbol	Parameter	Test Conditions	Limits		Units
			Min	Max	
t_{AS}	Address Setup Time	Input Rise and Fall Times (10% to 90%) 20ns	2		μS
$t_{OE\bar{H}}$	$\bar{O}E/V_{PP}$ Hold Time		2		μS
t_{OES}	$\bar{O}E/V_{PP}$ Setup Time		2		μS
t_{DS}	Data Setup Time		2		μS
t_{AH}	Address Hold Time	Input Pulse Levels 0.45V to 2.4V	0		μS
t_{DH}	Data Hold Time		2		μS
t_{DFP}	$\bar{O}E/V_{PP}$ High to Output Float Delay ⁽²⁾		0	130	ns
t_{VCS}	V _{CC} Setup Time	Input Timing Reference Level 0.8V to 2.0V	2		μS
t_{PW}	$\bar{C}E$ Program Pulse Width ⁽³⁾		95	105	μS
t_{DV}	Data Valid from $\bar{C}E$			150	ns
t_{PRT}	V _{PP} Pulse Rise Time During Programming	Output Timing Reference Level 0.8V to 2.0V	50		ns
t_{VR}	$\bar{O}E/V_{PP}$ Recover time		2		μS

Notes: 1. V_{CC} must be applied simultaneously or before $\bar{O}E/V_{PP}$ and removed simultaneously or after $\bar{O}E/V_{PP}$

2. This parameter is only sampled and is not 100% tested. Output Float is defined as the point where data is no longer driven – see timing diagram.

3. Program Pulse width tolerance is 100 $\mu sec \pm 5\%$.

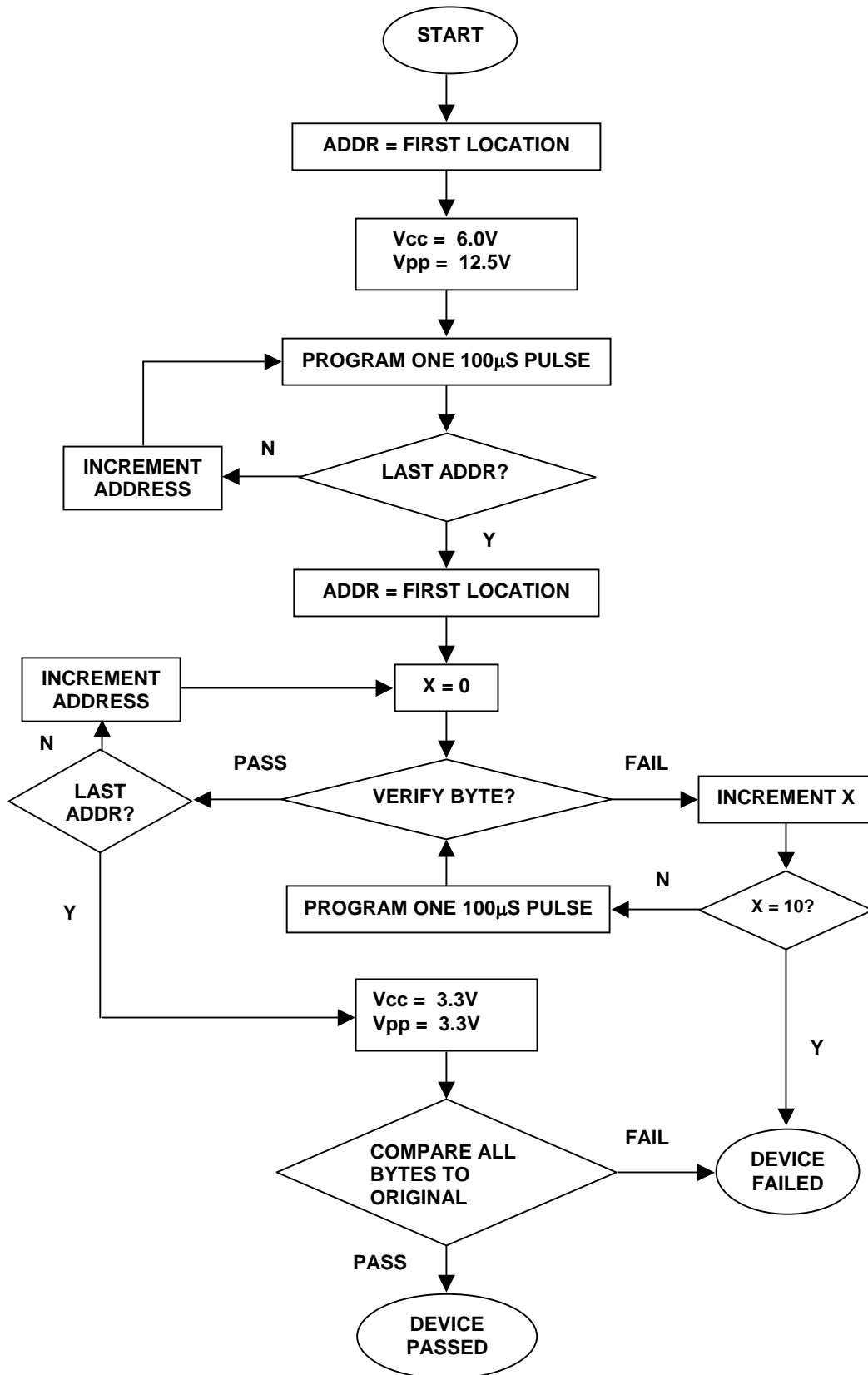
Product Identification Code

Codes	Pins										Hex Data
	A1	A0	O7	O6	O5	O4	O3	O2	O1	O0	
Continue Code 1	0	0	0	1	1	1	1	1	1	1	7F
Continue Code 2	0	1	0	1	1	1	1	1	1	1	7F
Manufacturer	1	0	0	1	0	1	1	1	1	0	5E
Device Type	1	1	1	1	0	0	0	0	0	0	C0

Rapid Programming Algorithm

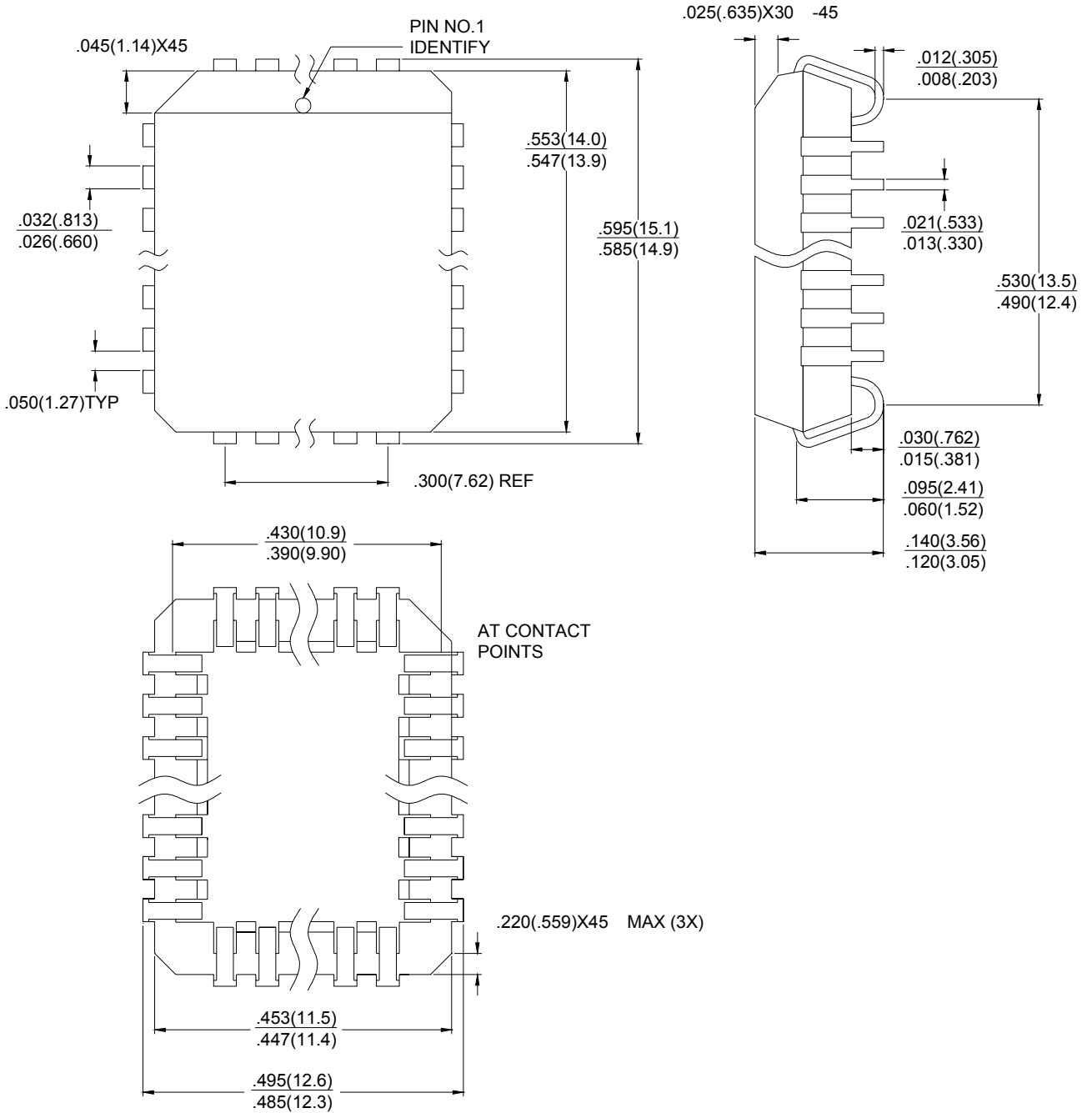
A 100 μs $\bar{C}E$ pulse width is used to program. The address is set to the first location. V_{CC} is raised to 6.0V and $\bar{O}E/V_{PP}$ is raised to 12.5V. Each address is first programmed with one 100 μs $\bar{C}E$ pulse without verification. Then a verification reprogramming loop is executed for each address. In the event a byte fails to pass verification, up to 10 successive 100 μs pulses are applied with a verification after each pulse. If the byte fails to verify after 10 pulses have been applied, the part is considered failed. After the byte verifies properly, the next address is selected until all have been checked. All bytes are read again and compared with the original data to determine if the device passes or fails.

Fast Programming Flowchart

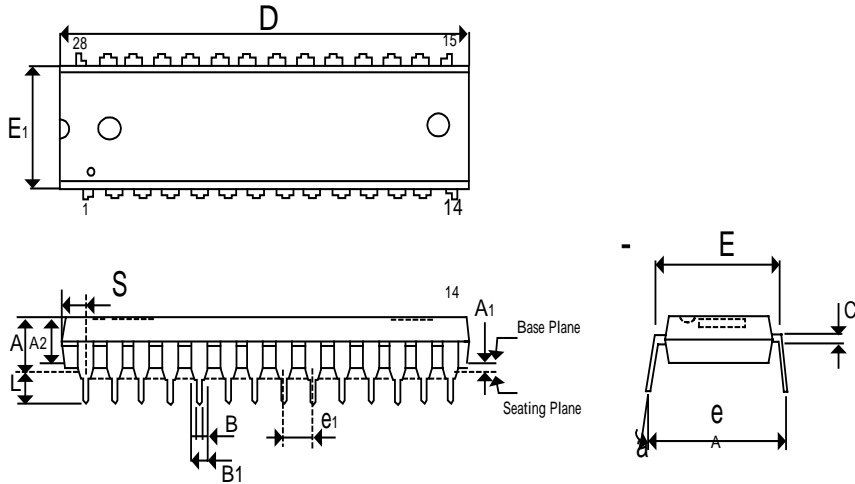


Packaging Information

32P, 32-Lead, Plastic J-Leaded Chip Carrier (PLCC)
 Dimensions in Inches and (Millimeters)
 JEDEC STANDARD MS-016 AE

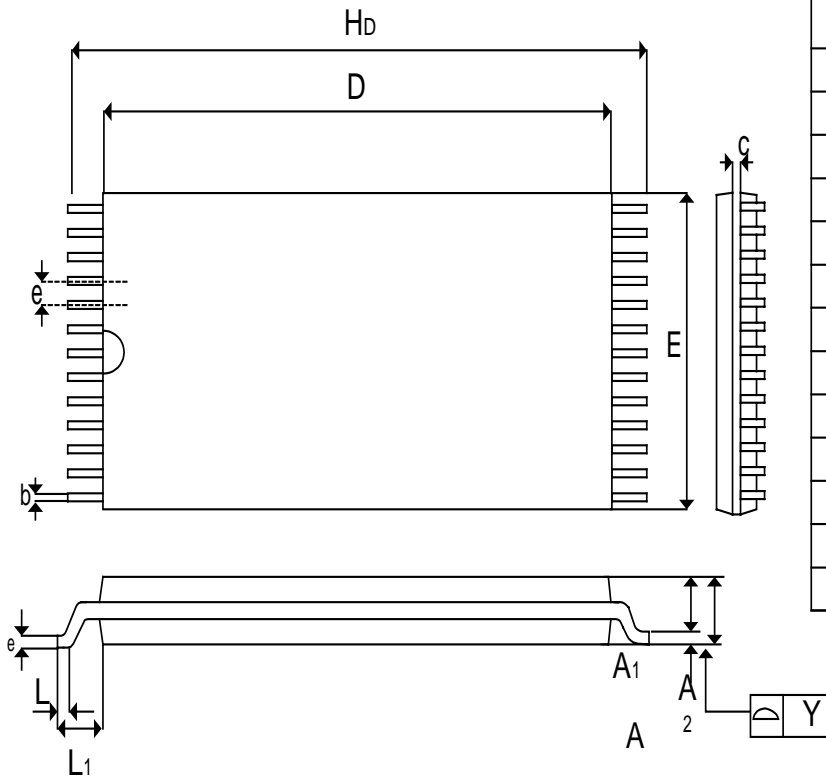


28Pin, 28-Lead, 0.600" wide, Plastic Dual Inline Package (PDIP)
 Dimensions in Inches and (Millimeters)



Symbol	Dimension in mm		
	Min.	Nom.	Max.
A	-	-	5.59
A ₁	0.38	-	-
A ₂	3.71	3.91	4.11
B	0.36	0.46	0.56
B ₁	1.02	1.27	1.65
C	0.20	0.25	0.33
D	36.58	37.14	37.34
E	15.24	-	15.88
E ₁	13.64	13.89	14.15
e ₁	-	2.54	-
eA	15.88	-	16.89
L ₁	3.18	-	4.06

28Pin, 28-Lead, Plastic Thin Small Outline Package (TSOP)
 Dimensions in Millimeters and (Inches)*
 JEDEC OUTLINE MO- 141 BD



Symbol	Dimension in Inches			Dimension in mm		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	0.047	-	-	1.20
A ₁	0.002	-	0.006	0.05	-	0.15
A ₂	0.035	0.040	0.041	0.95	1.00	1.05
b	0.007	0.008	0.011	0.17	0.20	0.27
c	0.004	0.006	0.008	0.10	0.15	0.21
D	0.461	0.465	0.469	11.70	11.80	11.90
E	0.311	0.315	0.319	7.90	8.00	8.10
H _D	0.520	0.528	0.536	13.20	13.40	13.60
e	-	0.022	-	-	0.55	-
L	0.020	0.024	0.028	0.50	0.60	0.70
L ₁	-	0.010	-	-	0.25	-
Y	0.000	-	0.004	0.00	-	0.10
	0	3	5	0	3	5

Note: Controlling dimension: Millimeters

PRODUCTION ORDERING INFORMATION

Example

ICE27LC512 - XX XX

